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<u>L21</u>	L20 and l4	420	<u>L21</u>
<u>L20</u>	L19 and l2	768	<u>L20</u>
<u>L19</u>	L17 and l9	799	<u>L19</u>
<u>L18</u>	L17 and l13	31	<u>L18</u>
<u>L17</u>	print job and print data	1464	<u>L17</u>

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<u>L16</u>	L15 and l13	21	<u>L16</u>
<u>L15</u>	print job and print data	607	<u>L15</u>

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<u>L14</u>	L13 and l9	60	<u>L14</u>
<u>L13</u>	l10 same releas\$3	491	<u>L13</u>
<u>L12</u>	L11 and queue	319	<u>L12</u>
<u>L11</u>	L10 and l9	1266	<u>L11</u>
<u>L10</u>	print with (hold or wait)	13078	<u>L10</u>
<u>L9</u>	L6 same l5 same (l3 or l2 or l4)	31884	<u>L9</u>
<u>L8</u>	L6 same l4 same (l3 or l2 or l4)	41598	<u>L8</u>
<u>L7</u>	L6 and l5 and l4 and l3 and l2	778	<u>L7</u>
<u>L6</u>	queue or memory or spool\$3	1459564	<u>L6</u>
<u>L5</u>	printer	441356	<u>L5</u>
<u>L4</u>	server	189857	<u>L4</u>
<u>L3</u>	rip or rasterizer	14253	<u>L3</u>
<u>L2</u>	processor or controller or cpu or processing unit	1757154	<u>L2</u>

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<u>L1</u>	device control filter	3	<u>L1</u>
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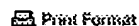
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Bergman, C.A.;

Southern Tier Technical Conference, 1990., Proceedings of the 1990 IEEE , 25 April 1990

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2 **A simulation case study of production planning and control in printed wiring board manufacturing**

Korhonen, H.M.E.; Heikkila, J.; Tornwall, J.M.;

Simulation Conference, 2001. Proceedings of the Winter , Volume: 2 , 9-12 Dec. 2001

Page(s): 844 -847 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(343 KB\)\]](#) **IEEE CNF**

3 **An on-line batching algorithm for a single automatic insertion machine with fixed cost setups**

Lofgren, C.B.;

Electronic Manufacturing Technology Symposium, 1988, 'Design-to-Manufacturing Transfer Cycle'. Fifth IEEE/CHMT International , 10-12 Oct. 1988

Page(s): 188 -191

[\[Abstract\]](#) [\[PDF Full-Text \(260 KB\)\]](#) **IEEE CNF**

4 **A functional approach to simulation programming**

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1 Pattern placement for high speed printers

Jorgens, D.;

CompEuro '89., 'VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks', Proceedings. , 8-12 May 1989

Page(s): 2/70 -2/74

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **IEEE CNF**

2 Using boundary scan test to test random access memory clusters

Muris, M.; Biewenga, A.;

Test Conference, 1993. Proceedings., International , 17-21 Oct. 1993

Page(s): 174 -179

[\[Abstract\]](#) [\[PDF Full-Text \(400 KB\)\]](#) **IEEE CNF**

3 Image compression for memory constrained printers

Cosman, P.; Frajka, T.; Zeger, K.;

Image Processing, 1998. ICIP 98. Proceedings. 1998 International Conference on , 4-7 Oct. 1998

Page(s): 109 -113 vol.3

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4 Higher density using diffusion patterned vias and fine line printing

Bender, D.K.; Ferreira, A.M.;